

AMENDMENTS TO THE SPECIFICATION

Please amend the Specification as follows:

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On page 29, please amend the paragraph beginning on line 8 as follows:

The selection voltage supplied from the gate driver circuit 13 is a pulsed signal voltage having a pulse width corresponding to the "horizontal synchronizing period". In the horizontal synchronizing period, all the TFTs 15 connected to the said scanning line 17 are kept in the conducting (i.e., selected) state and therefore, the data voltages from the source driver circuit 14 are applied to the respective pixel electrodes ~~24~~²³ connected to the said TFTs 15.